

Editorial Comments:

Concerning the paper my remarks:

The work concerns the analysis of the storage of charges in capacitors. The authors focused on the mathematical and technical aspects of calculating electrical quantities, when charging and discharging capacity. I think that the results obtained and the manuscript must be valued and improved. I propose:

- the abstract to be reduced by showing the work to be done in a bibliographical context (Without equations),

Author feedback: I have done the correction

- Introduction: mention the use field of these capacities (electrical components, integrated circuits ...) and the problems (breakdown, ...).

Author feedback: I have included this note on the Introduction about - classically the field quantities are related by differential equations and here the fractional differential equations (i.e. integral equations) are used, also elaborated the problem in capacitor break down-encountered in electronic circuits.

- Put a diagram that illustrates the structure of the capacity. Try to discuss the structure of the capacity in relation to the problems of this component (Introduction).

Author feedback: I have changed the figure and the figure includes the photo of testing set up, with schematic and the structure of voltage profiles that we observe in reality for a fractional capacitor. Also included this aspect in introduction.

- The authors present only one figure (Figure 1). I think it's not enough. The authors must add other figures or curves to enhance their calculation techniques. A comparison with classical calculation is essential.

Author feedback: I have changed with a new figure and in text I have mentioned how the voltage profiles would have been if the capacitor were ideal text-book type.

- The authors evoke the breakdown of capacities but no results are presented in this sense ?.

Author feedback: I have referred citations where the charge expected is more than the classical charge storage of equilibrium charge, -in studies of dielectric. This shows that while capacitor is at constant voltage, the charges do not reach equilibrium. I have also pointed out about the failure of DC link capacitors in electronic circuits, where the capacitor were never stressed at all to its maximum break down voltage limit.

- Paragraph 10 (page 23): the authors must put pictures and circuits of the measures bench used, thus the results obtained. Also, add a paragraph to validate the equations and plots of Figure 1.

Author feedback: I have included Picture of Photograph of Test set-up. The voltage profile is validated by use of formula mentioned, and the formulas used to find fractional order and fractional capacity of a capacitor.

- The conclusion to be reduced by showing the originality of the work.

Author feedback: I have reduced the conclusion, and mentioned that is new formulation-and that is applied and verified with the existing concepts.

- Reduce the number of paper pages. 27 pages, are a lot !!!!

Author feedback: I have reduced the pages from 27 to 18